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Leakage Power Reduction and Power Delay Product (PDP ...

[The industry for low power Nitesh Kumar Kiran, Department of Electronics & Telecommunication Engineering, SSCET, Bhilai., Durg, India, progressively more important issue in processor hardware and software design. A. Power Dissipation Power is defined as the rate at which the energy is transferred or exchanged in the circuit. Power dissipation in the circuit is defined as the rate at which](#)

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Integrated Hardware-Software Co-Synthesis and High-Level ...

Therefore, we believe that hardware-software co-synthesis for low-power embedded systems should be performed in tight integration with high level synthesis (HLS) under the guidance of an accurate timing and power evaluation (estimation) mechanism. This is the topic of this paper. Many of the existing hardware-software co-synthesis methodologies [1] [8] [18] contemplate an abstract model